Communication

Press Play: Interactive Device Design | July 21, 2011

Check In: MP3 Player Designs

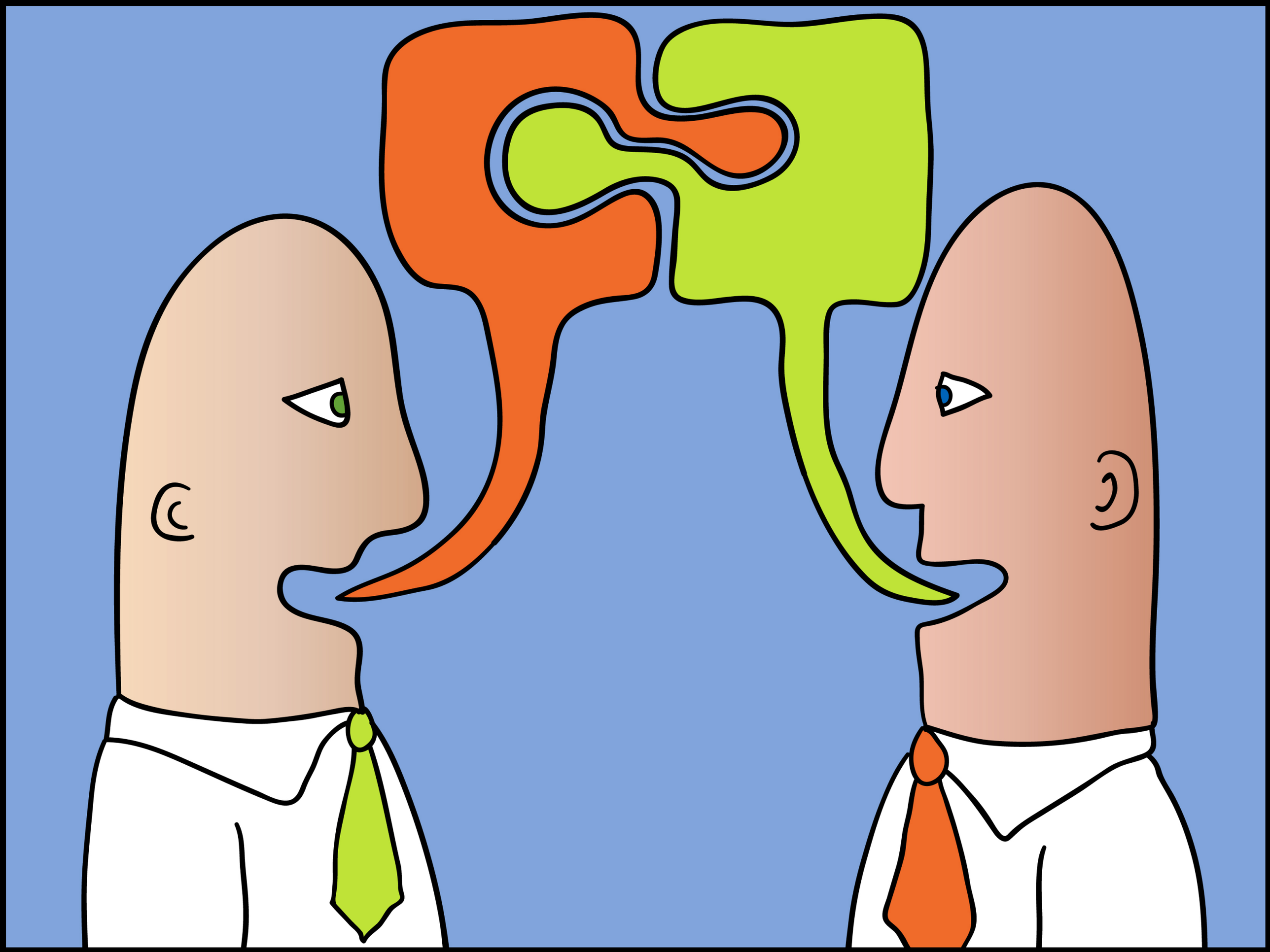
Start with the big picture: what's it going to do?

Mine your observations or own needs for ideas.

Given that, what functions should it perform?

What interactions do those functions imply?

Map/sketch/diagram how those will work.



Context of Communication

Conversation - Rules of Conduct

Communication is holding a conversation

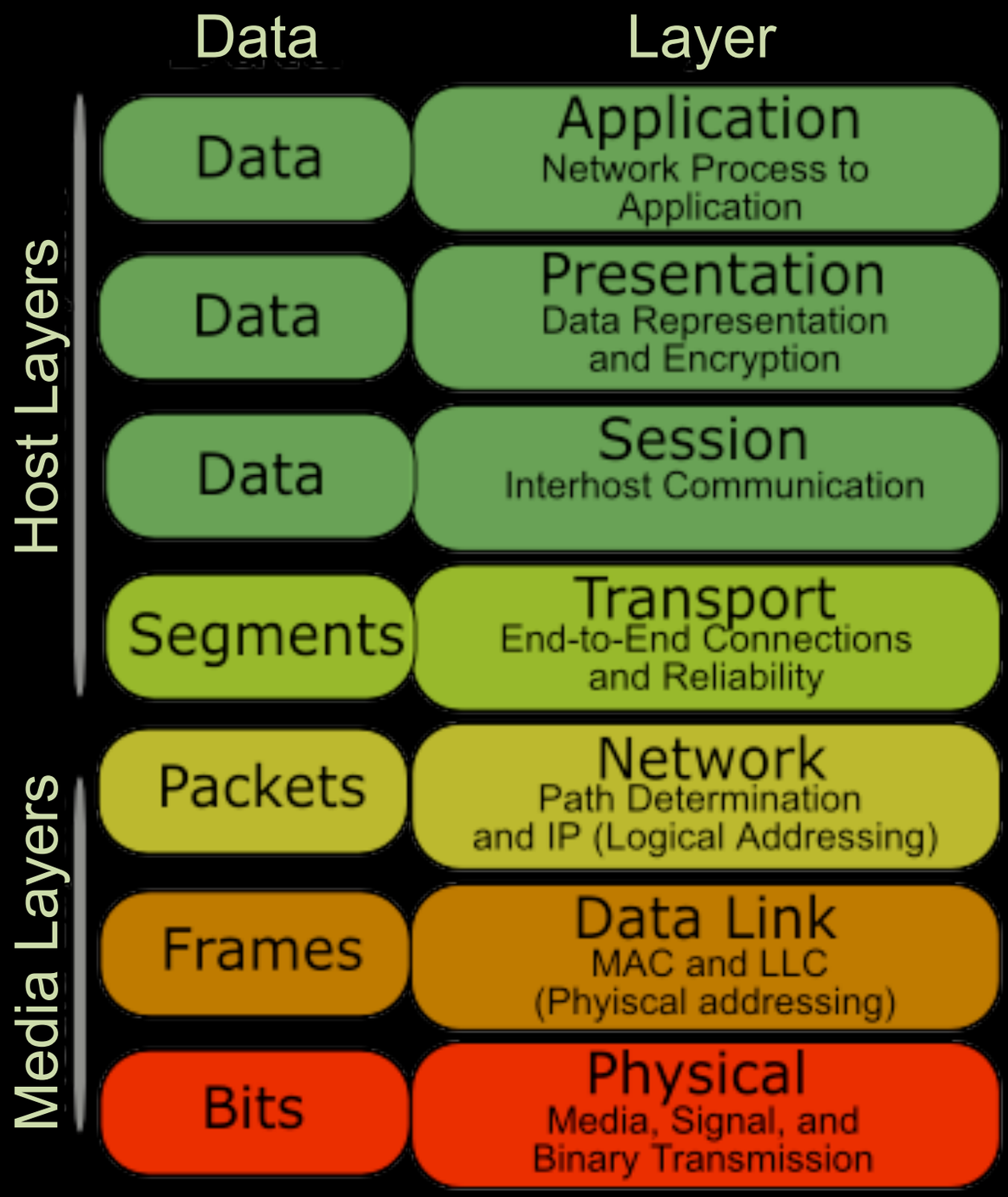
- Inter-processor communication is ‘peer-to-peer’
- Processor-to-device conversation is ‘master-slave’

A protocol is a set of rules of conduct that we agree to uphold during the conversation

- It governs how we start a conversation, who speaks when, how fast, how often, etc.

Context of Communication

Open Systems Interconnection



Serial, WiFi, Ethernet

Voltage, Wires, Timing

Types of Interface

Parallel

Examples

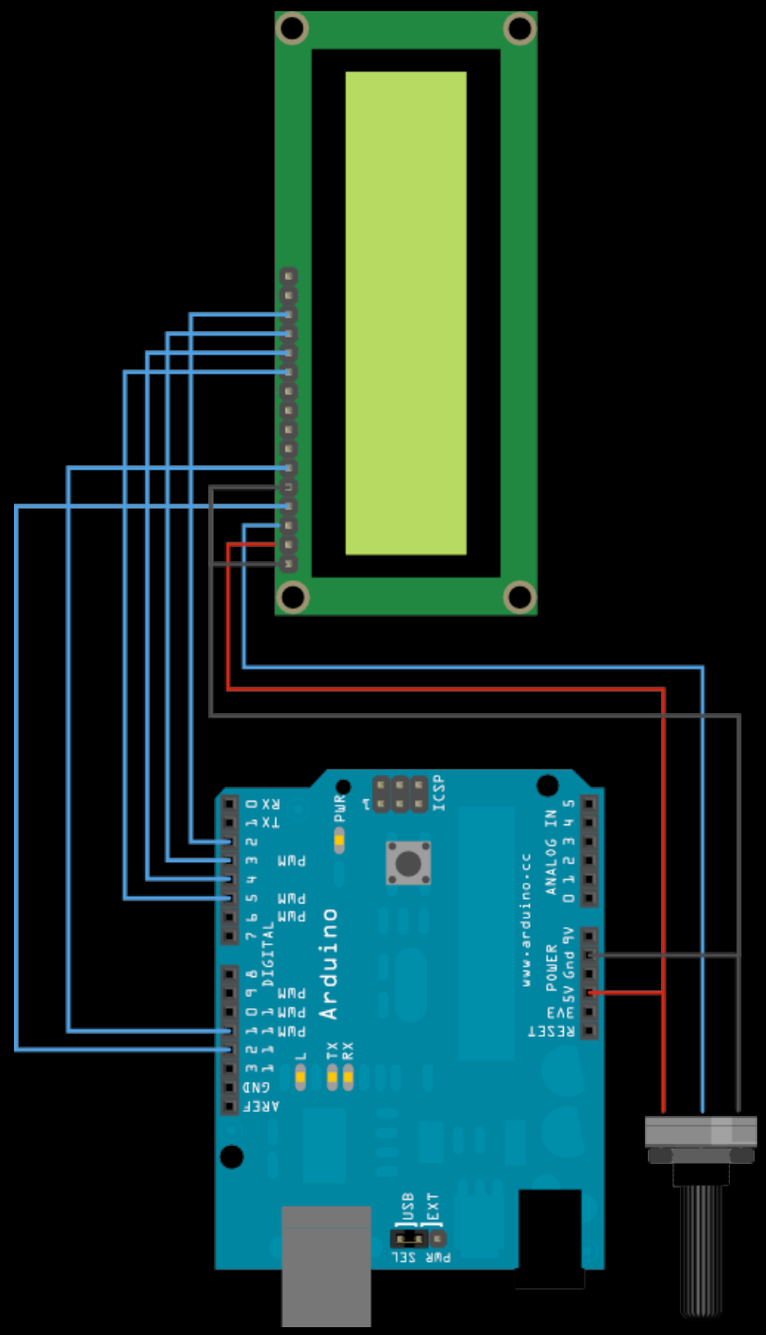
- Graphical LCD
- SCSI, Firewire

Advantages

- Faster in Theory

Drawbacks

- Crosstalk
- Clock Skew
- Wire per Bit



Types of Interface

Serial

Examples

- USB, SATA
- SPI and I^2C

Advantages

- Clock Faster
- Fewer Wires

Drawbacks

- Overhead of Negotiation

Conserve Resources

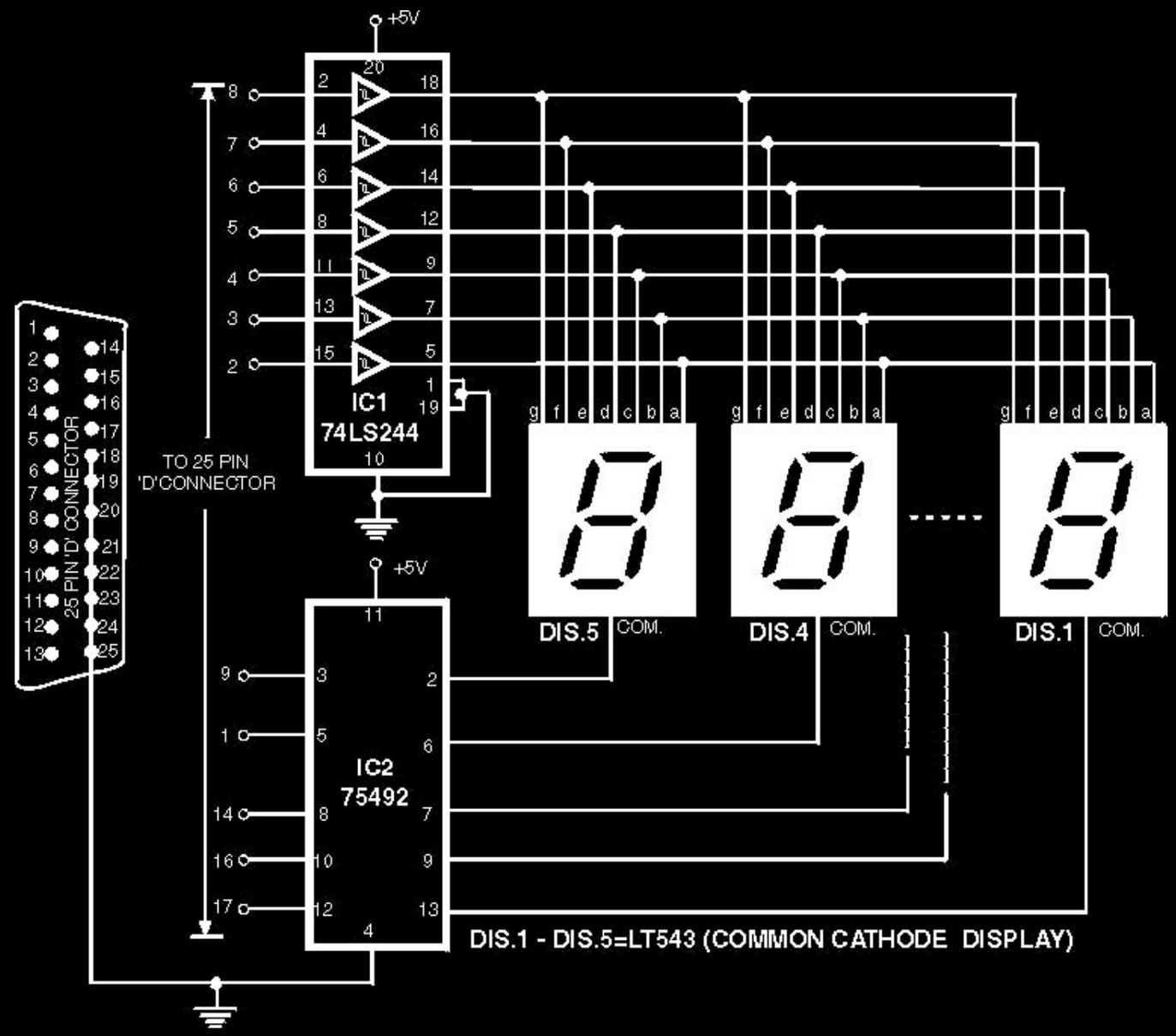
Ride the Bus

Types of Interface

- Parallel or Serial
- Internal or External

One Line per Device

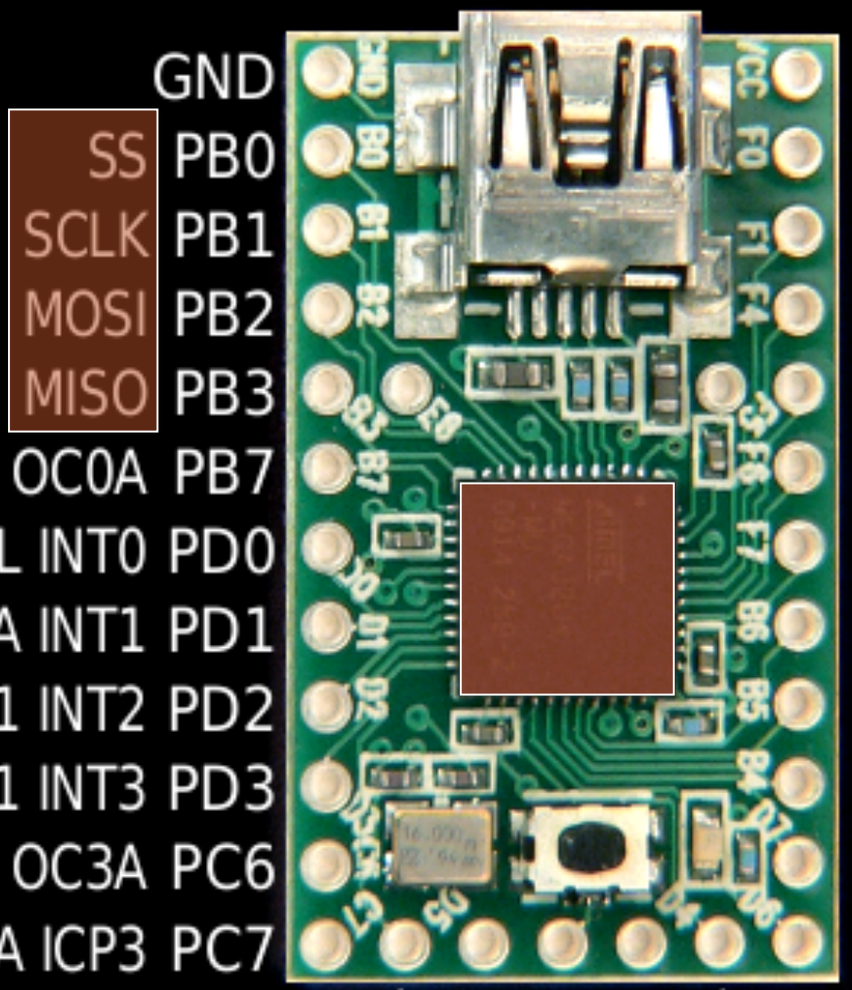
- Chip Select



Microprocessor

Communication

SPI Bus ATmega32U4



(interior)
PE6
AIN0
INT6

SS PB0
SCLK PB1
MOSI PB2
MISO PB3

RTS OC1C OC0A PB7
OC0B SCL INT0 PD0
SDA INT1 PD1
RXD1 INT2 PD2
TXD1 INT3 PD3
$\overline{\text{OC4A}}$ OC3A PC6
OC4A ICP3 PC7
CTS XCK1 PD5

GND
Vcc
GND
RST
Vcc

GND
PF0 ADC0
PF1 ADC1
PF4 ADC4
PF5 ADC5
PF6 ADC6
PF7 ADC7
PB6 ADC13 OC1B OC4B
PB5 ADC12 OC1A $\overline{\text{OC4B}}$
PB4 ADC11
PD7 ADC10 T0 OC4D
PD6 ADC9 T1 $\overline{\text{OC4D}}$
PD4 ADC8 ICP1

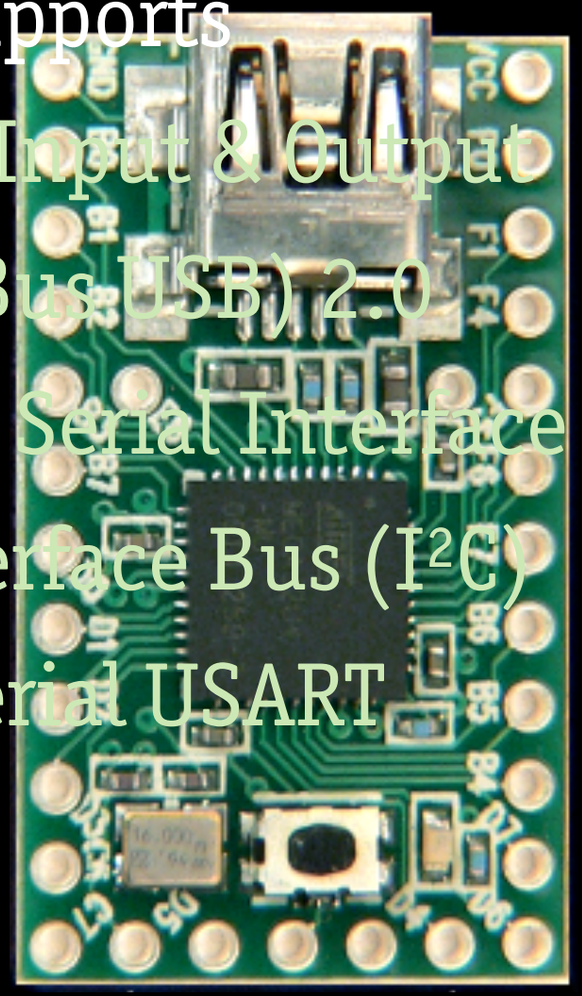
(LED on PD6)

(interior)
AREF

Microprocessor
Communication

The ATmega32U4 Supports

- Digital & Analog Input & Output
- Universal Serial Bus (USB) 2.0
- Master/Slave SPI Serial Interface
- 2-Wire Serial Interface Bus (I^2C)
- Programmable Serial USART



How Does an MCU Communicate?

Bits and Bytes

1 Bit

0 = LO

1 = HI

2 Bits

00 = 0

01 = 1

10 = 2

11 = 3

3 Bits

000 = 0 ◀

001 = 1

010 = 2 ◀

011 = 3

100 = 4 ◀

101 = 5

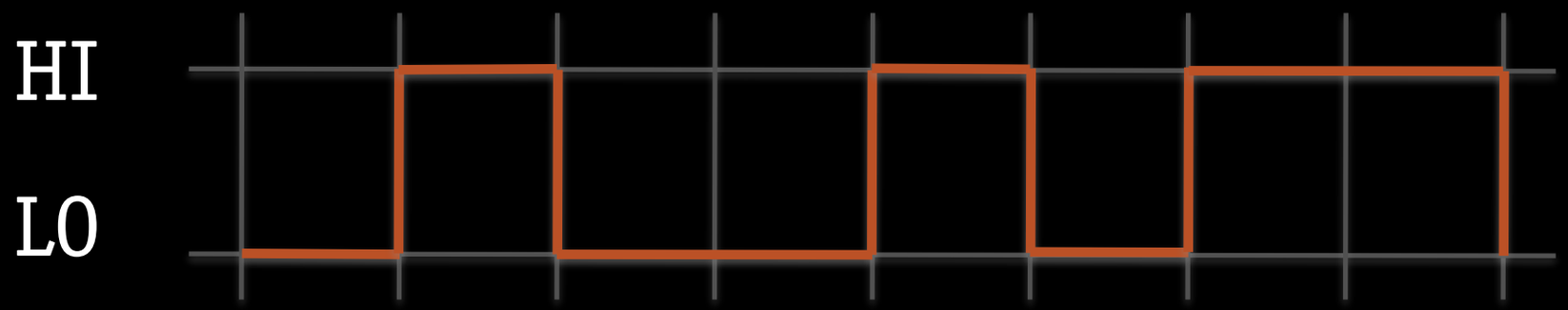
110 = 6

111 = 7

4 Bits

1000 = 8 ◀

...



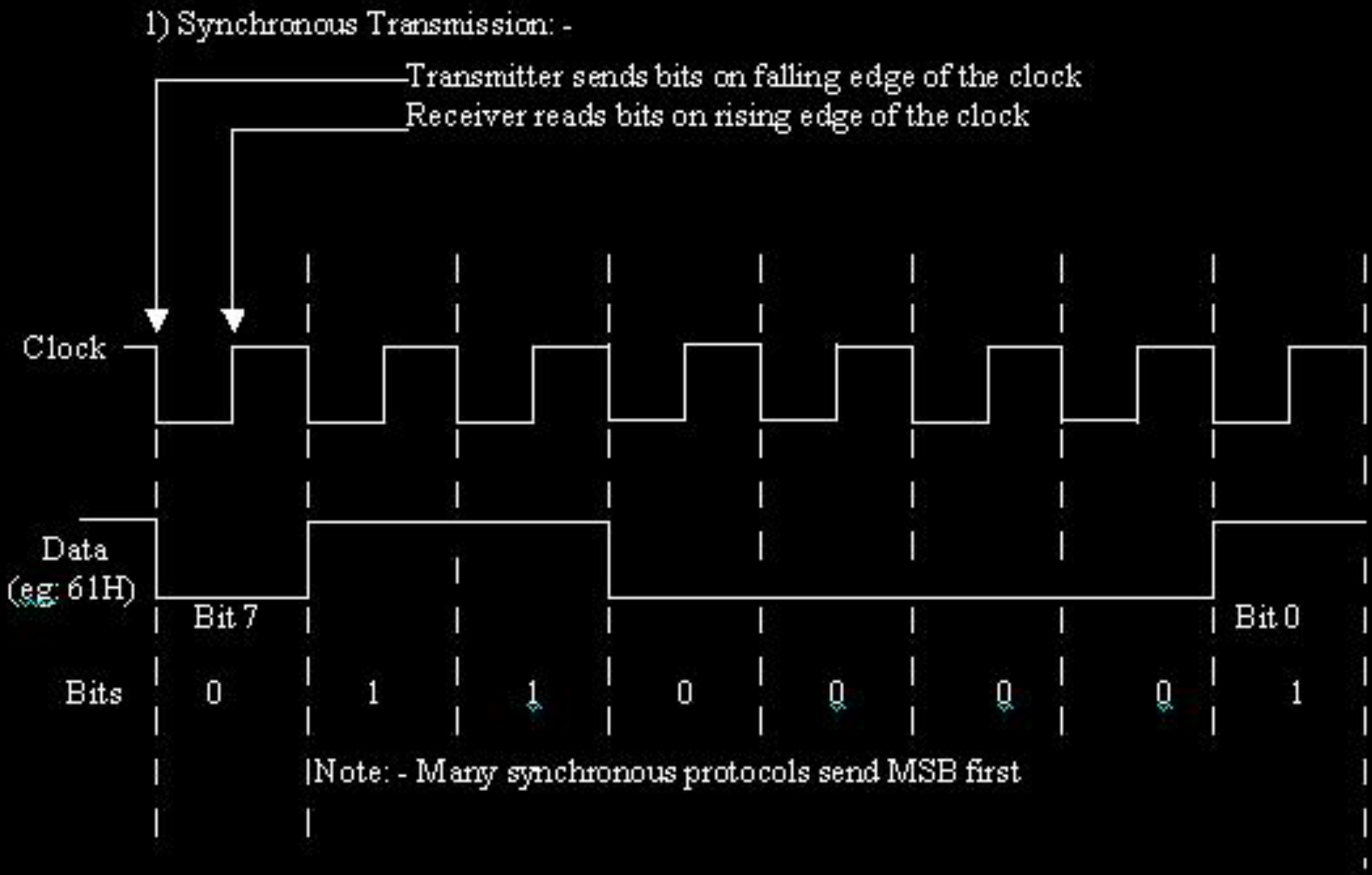
How Does an MCU Communicate?

Parallel or Serial

Synchronous or Asynchronous

Serial Communication

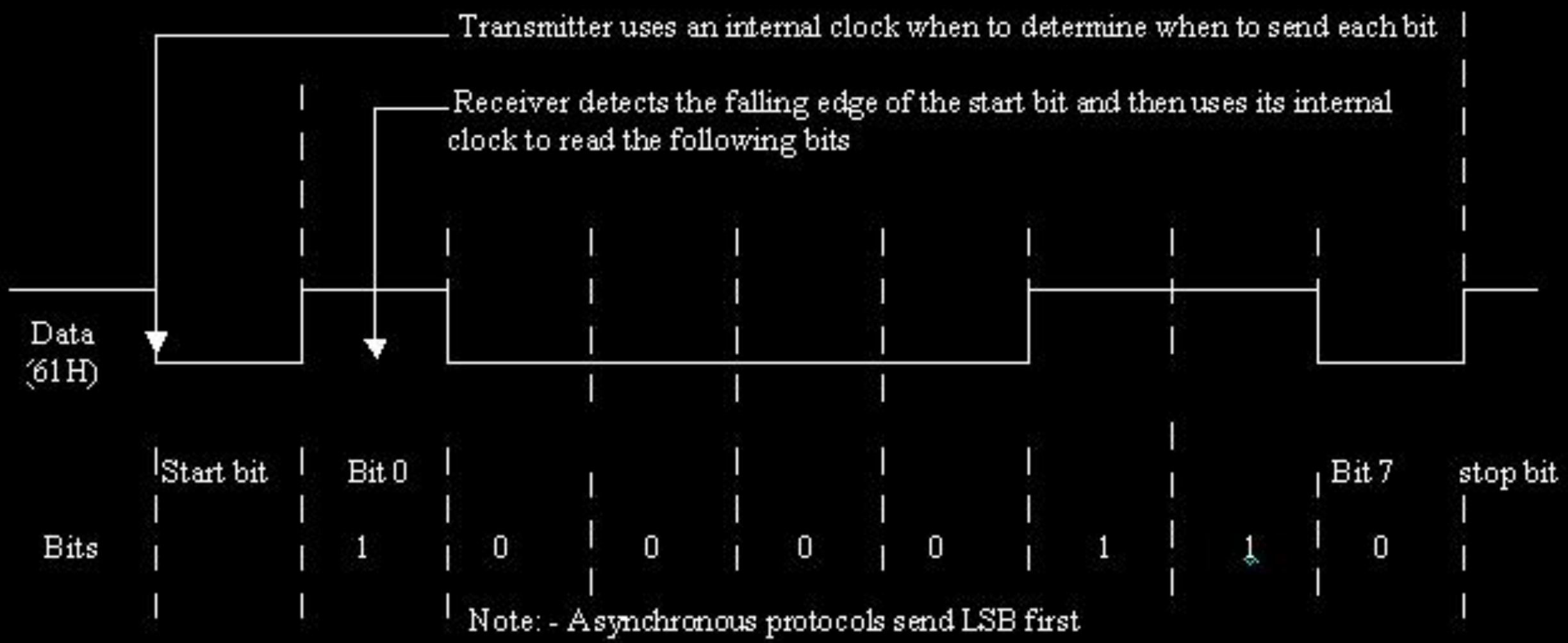
Synchronous



Serial Communication

Asynchronous

2) Asynchronous Transmission: -



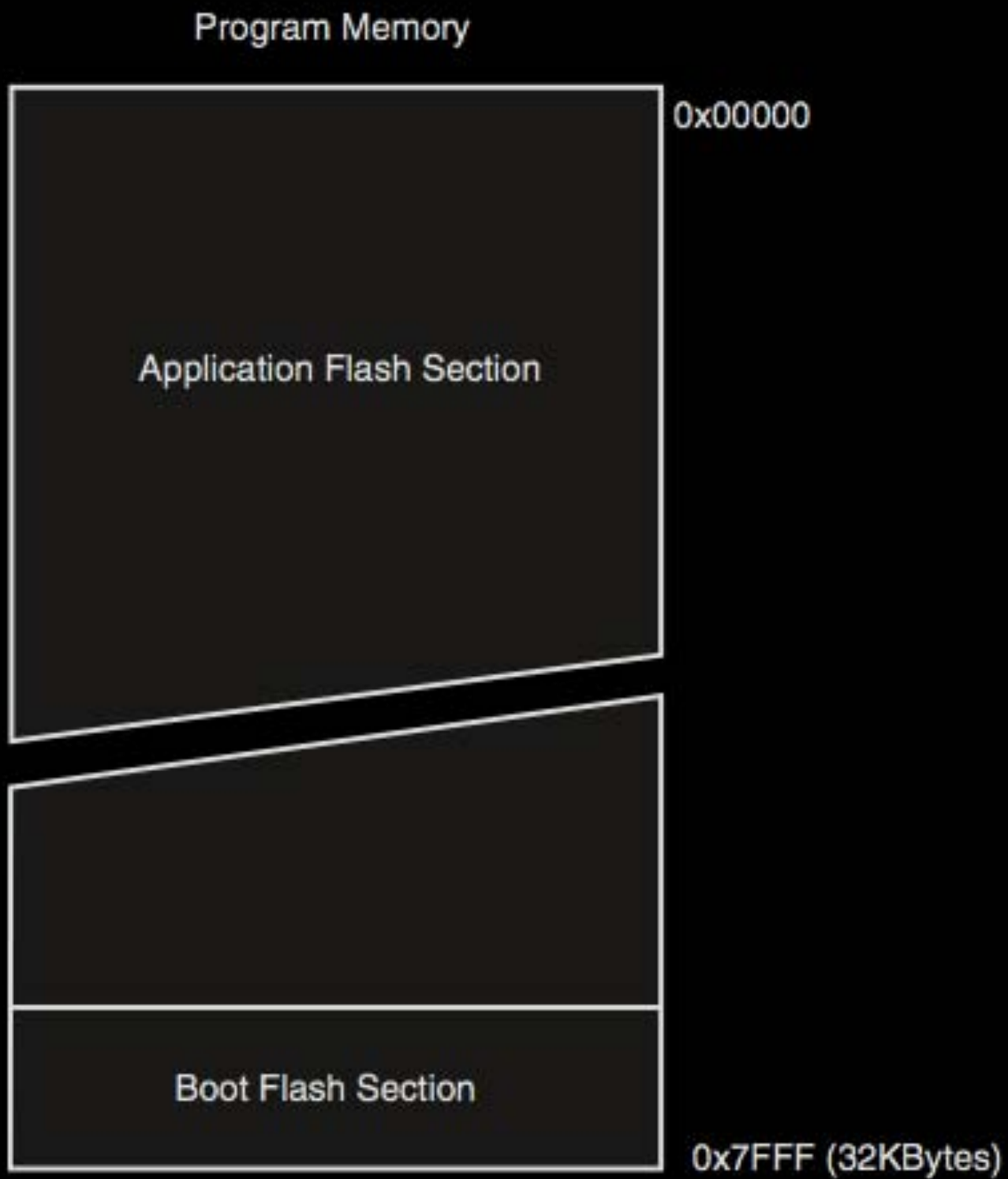
Serial Peripheral Interface

Configuration and Use

- How do we configure the MCU for SPI?
- How do we use SPI to communicate?

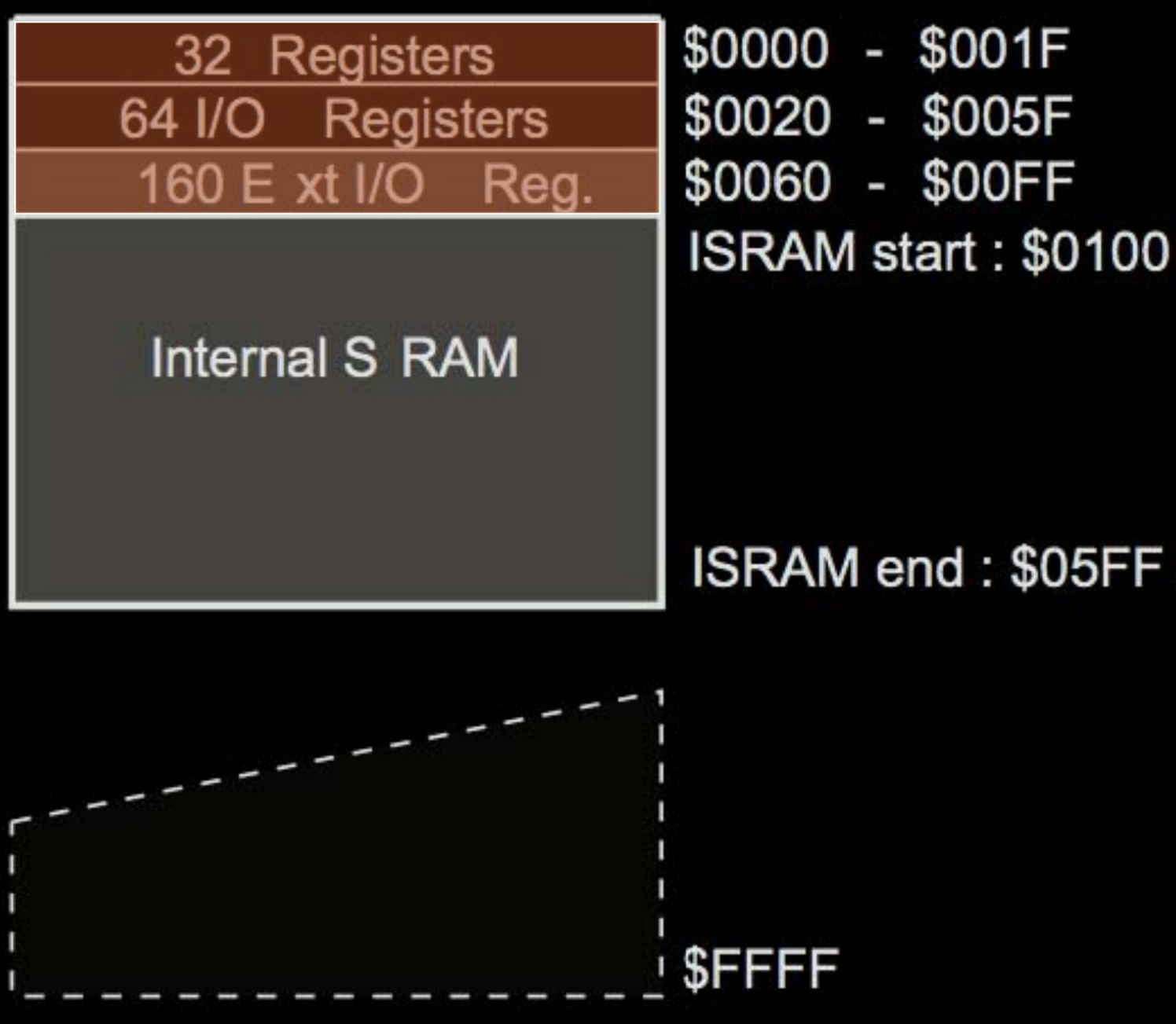
ATmega32U4 Memory Map

Program Memory



ATmega32U4 Memory Map

Data Memory



ATmega32U4 Memory Map

Access Data & Settings

- A register is a small amount of storage available on the CPU whose contents can be accessed more quickly than storage available elsewhere.
- Certain registers are reserved for MCU operating settings.
- Writing values into these registers changes the settings.

Serial Peripheral Interface

An Example of Configuration

SPI Control Register – SPCR

Bit	7	6	5	4	3	2	1	0	
	SPIE	SPE	DORD	MSTR	CPOL	CPHA	SPR1	SPR0	SPCR
Read/Write	R/W	R/W	R/W	R/W	R/W	R/W	R/W	R/W	
Initial Value	0	1	0	1	0	0	1	1	

Set SPCR = 0101 0000

SPE – “Enable SPI mode”

MSTR – “I control the clock”

Settings Registers

Bitwise Operators

How to set bits without over-writing an entire register?

Bitwise Operators	Bitwise AND	Bitwise OR
AND = “&”	0 & 0 = 0	0 | 0 = 0
OR = “|”	0 & 1 = 0	0 | 1 = 1
NOT = “~”	1 & 0 = 0	1 | 0 = 1
XOR = “^”	1 & 1 = 1	1 | 1 = 1

We set the SPCR using a bitwise OR: SPCR |= 0101 0000

Serial Peripheral Interface

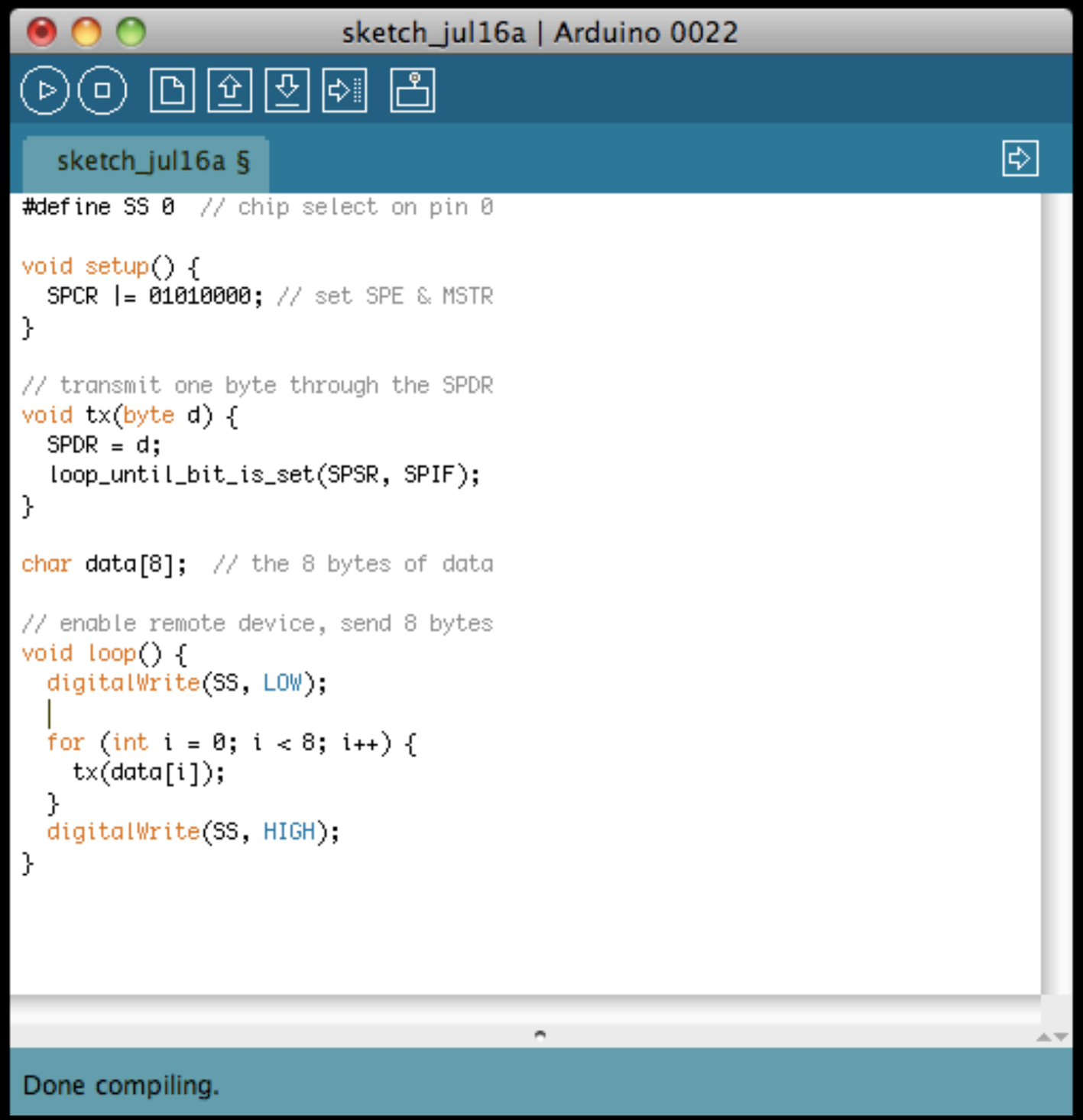
An Example of Use

Where are:

- SCLK
- MOSI
- MISO

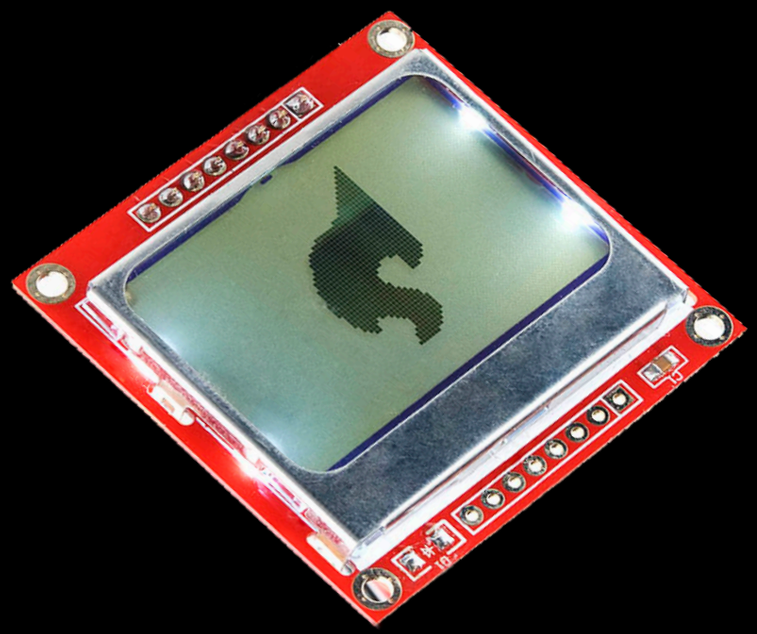
They are pre-assigned

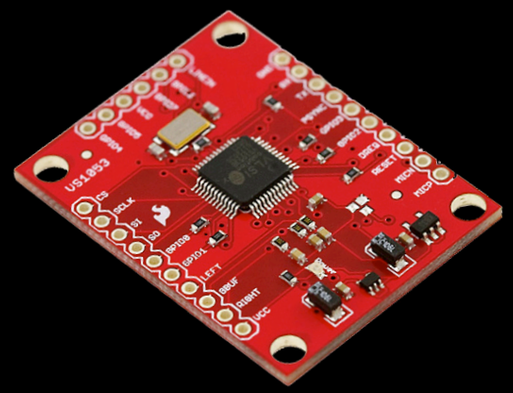
We only connect them



Communication Devices

Hardware You Can Use





Lab 5 Preview: Etch-a-Sketch

Modify Your Teensy to Output 3.3V

Writing to a Graphical LCD (GLCD)

Using a microSD Card for Storage